

# Intelligent EtherCAT® Master Board

## Low CPU load EtherCAT® Master Communication

### Features

#### Low CPU load EtherCAT® Master Communication

EtherCAT® environment is enabled typically by implementing the master stack on Ethernet hardware. Advanet provides EtherCAT® master communications on-board by implementing the Xilinx Zynq® with ARM® Cortex®-A9 on a board to minimize the impact for the host CPU as bus master.

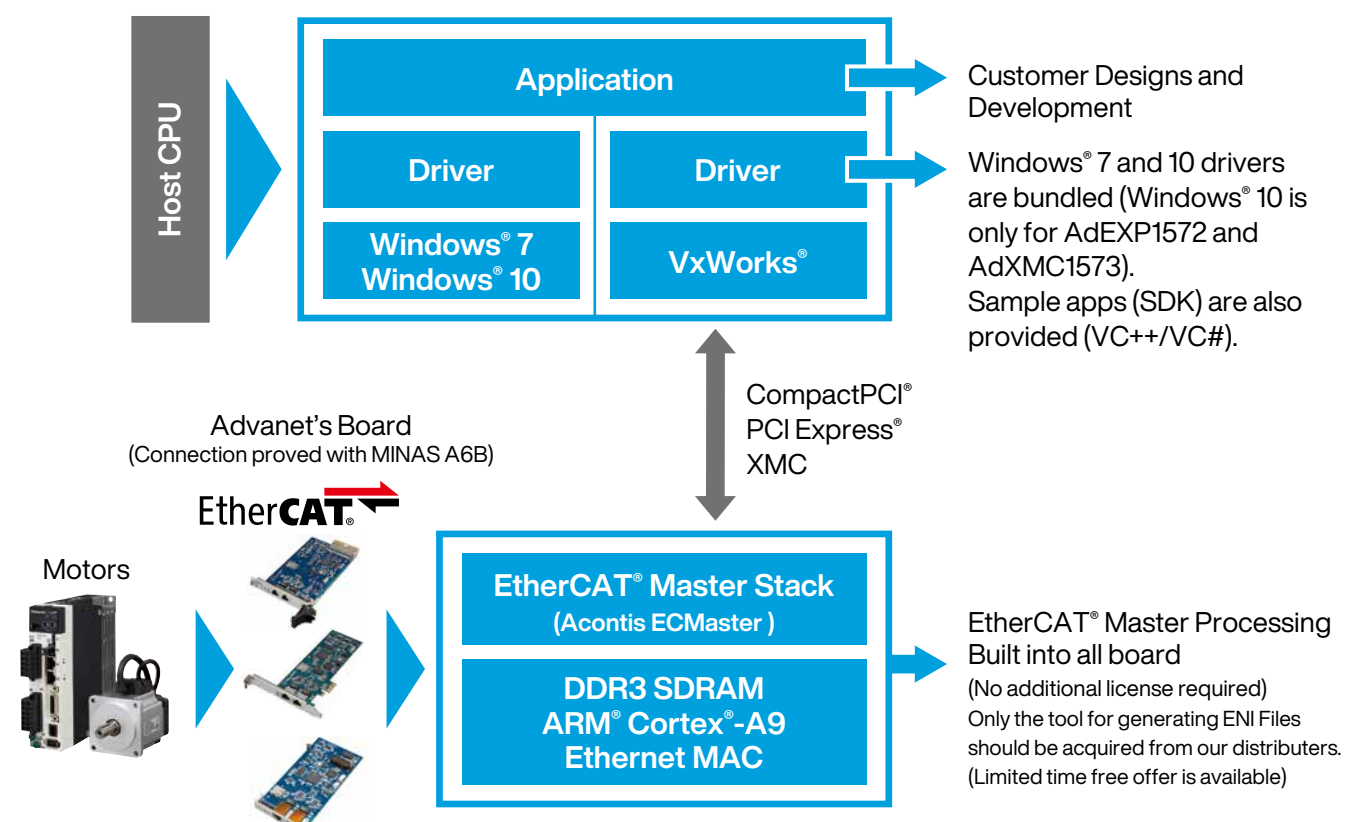
#### Secure Cable Redundancy

The redundant cable configuration adopting ring topology which recovers the communication cable failure in the EtherCAT® system allows the communications to reach any branch even in case of cable fracturing happened at any point.

#### Hot Connect Responds to Unexpected Replacement

The protocol of the EtherCAT® system utilizing a hot connect capability provides flexible and responsive functionalities to change the system configuration which allows you to connect/disconnect or reconfigure any part of the network “on-the-fly”.

## System Configuration



## Features of Advanet EtherCAT Slave

- Simultaneous measurement of 24ch in 1 slave
- Connectable 4-wire Pt100 or JPt100
- Temperature conversion on module
- Measurement overall precision at  $\pm 0.1^{\circ}\text{C}$  (\*Measurement range:  $-20^{\circ}\text{C}$  to  $80^{\circ}\text{C}$ )

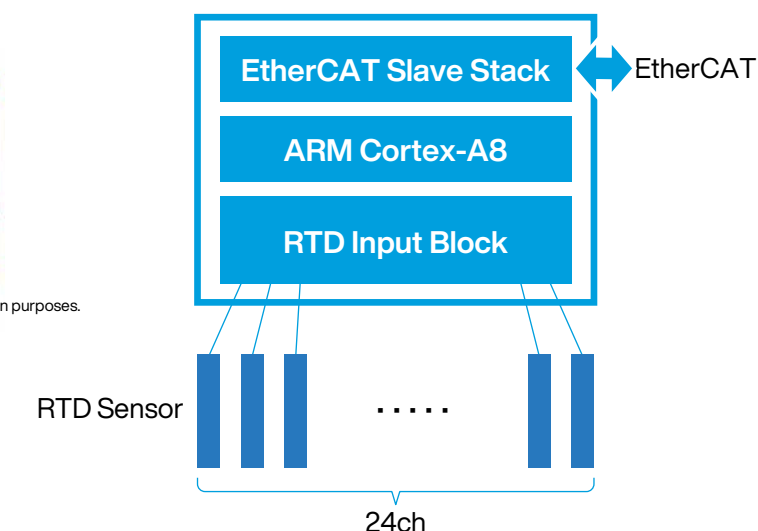
24-Channel RTD  
EtherCAT Slave Module  
**E-070**



Available  
Customization of  
EtherCAT Slave!



\* This image is for illustration purposes.



### Sales area

- Worldwide response except for some areas.

### Language

- Japanese
- English

### For more information

URL : <https://www.advanet.co.jp/ethercat/>



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