

For the traces in this portion, use internal layers and cover with Frame Ground plane.

RJ45

TX

CN1

TX+

TX-

TX+

TX-

RJ45

RX

CN2

RX+

RX-

RX+

RX-

VJQ855055113

VJQ855055113

Transformer

TR1

VST1A6T118LP

4.87K ±1%

2.2K

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Tie the connector shell to Frame Ground.

Void +3.3V and Ground planes underneath the transformer.

Tie to Frame Ground.

If using DP83848C instead of DP83848L, leave pin8 to 12 unconnected.

PHY

IC1

VOCCDP83848IVV

U_COL/PHYAD0

U_CRS/CRS_DV/LKD_CFG

U_CRS/CRS_DV/LKD_CFG

U_CRS/CRS_DV/LKD_CFG

U_CRS/CRS_DV/LKD_CFG

U_CRS/CRS_DV/LKD_CFG

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U_CRS/CRS_DV/LKD_CFG

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U_CRS/CRS_DV/LKD_CFG

ASIC

IC3

VOCCMM1221

VDDO

VDD1

VDD2

VDD3

VDD4

VDD5

VDD6

VDD7

VDD8

VDD9

VDD10

VDD11

VDD12

VDD13

VDD14

VDD15

VDD16

VDD17

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VDD19

VDD20

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VDD31

VDD32

VDD33

VDD34

VDD35

VDD36

VDD37

VDD38

VDD39

VDD40

VDD41

VDD42

U_i with pull-up 50k

D_i with pull-down 50k

T_15V Tolerant

T_15V Tolerant

T_15V Tolerant

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T_15V Tolerant

"IN" and "OUT" are alternative.

Protection against ESD.

IC2

VDD1

VSS

NC

OUT

VOCC81288N+01

RESET IC

td=100ms

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

VOCC81288N+01

PK100	CIRCUIT BOARD	581D373X	RELAT	1	KO	8
REF No.	PARTS NAME	CODE NO.	Relation	QTY	UNIT	TYPE
Model	MNM1221					
Name	Reference Circuit(I/O)					
No.	SR-MNM1221-IO					
Scale	Designed	Drawn	Checked	Approved	Sheet	
	FUJISAKI				1/2	
DATE	REVISION	OLD No	SIGNED	CHECKED		
2011/01/14						

Rev. 2

"IN" and "OUT" are alternative.
Install either "IN" or "OUT" parts.

24V
I/F

OUT

IN

Adjust filter values
according to used environment

Indicator

Install either J211 or J212.
If placing J211,
outputs will be forced to OFF
in RTEX timeout or not established.

Scale	Designed	Drawn	Checked	Approved	Sheet	Model	MNM1221
						Name	Reference Circuit(I/O)
						No.	SR-MNM1221-IO